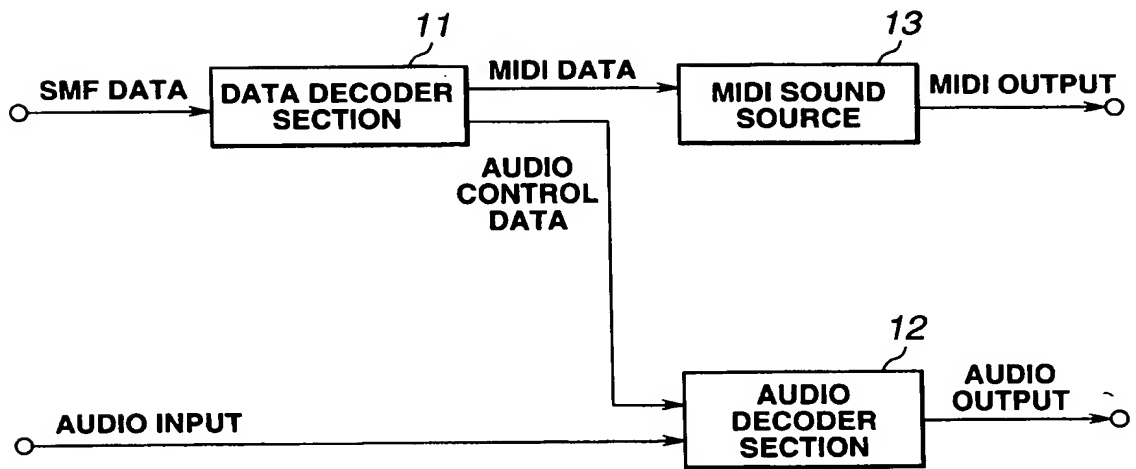


10 DIGITAL SIGNAL PROCESSING APPARATUS**FIG.1**

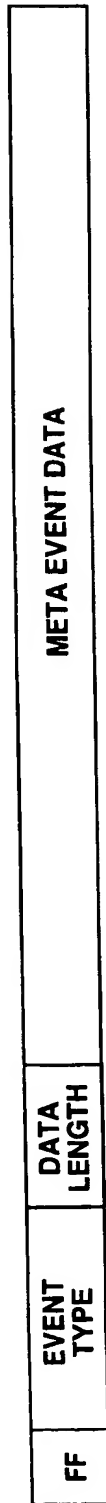


FIG.2

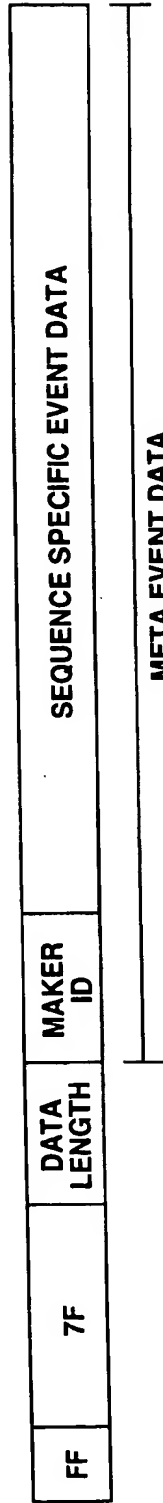


FIG.3

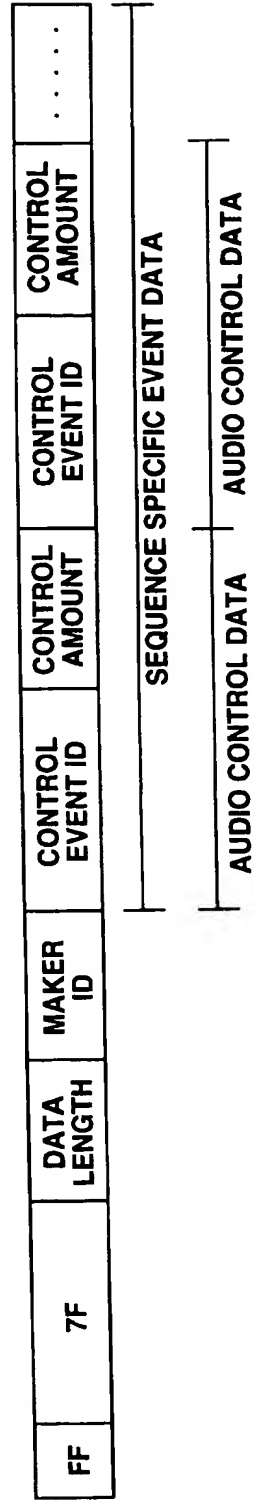


FIG.4

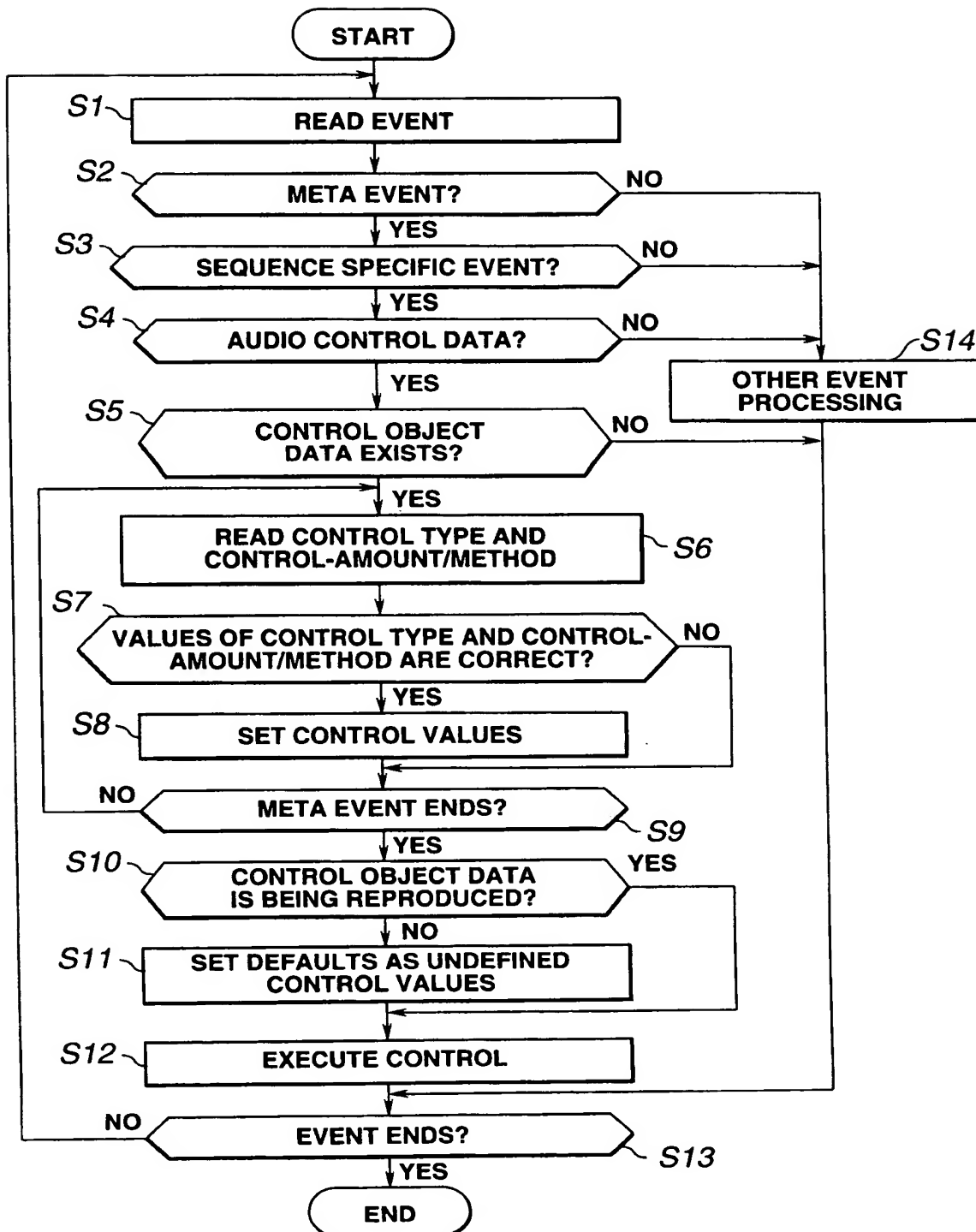


FIG.5

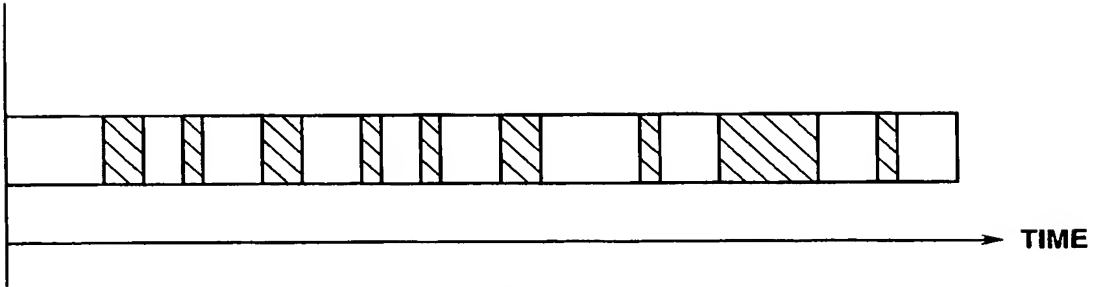


FIG. 6A

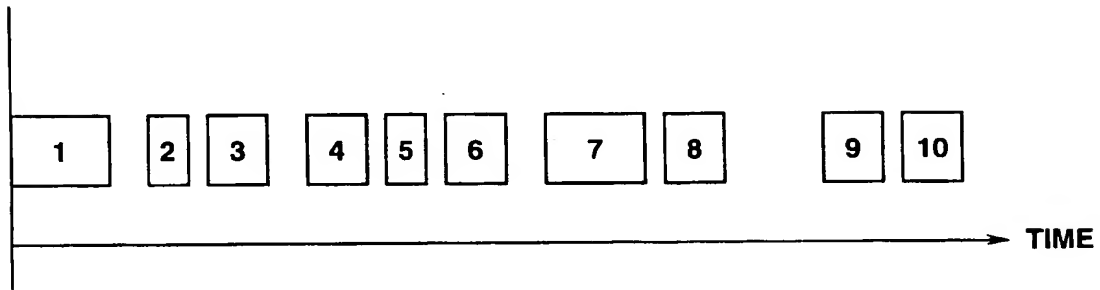


FIG. 6B

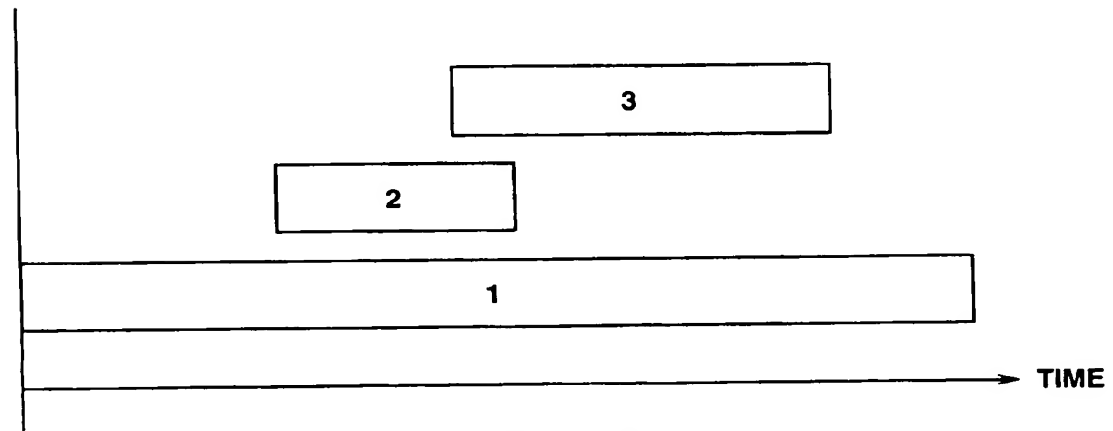


FIG. 7

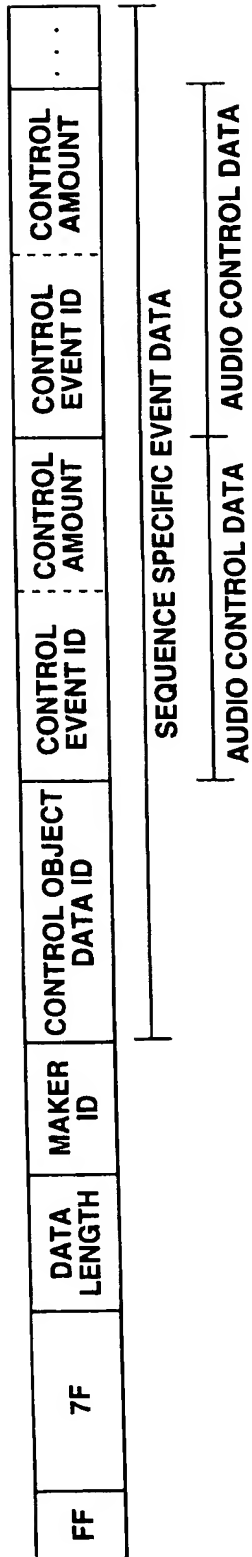


FIG.8

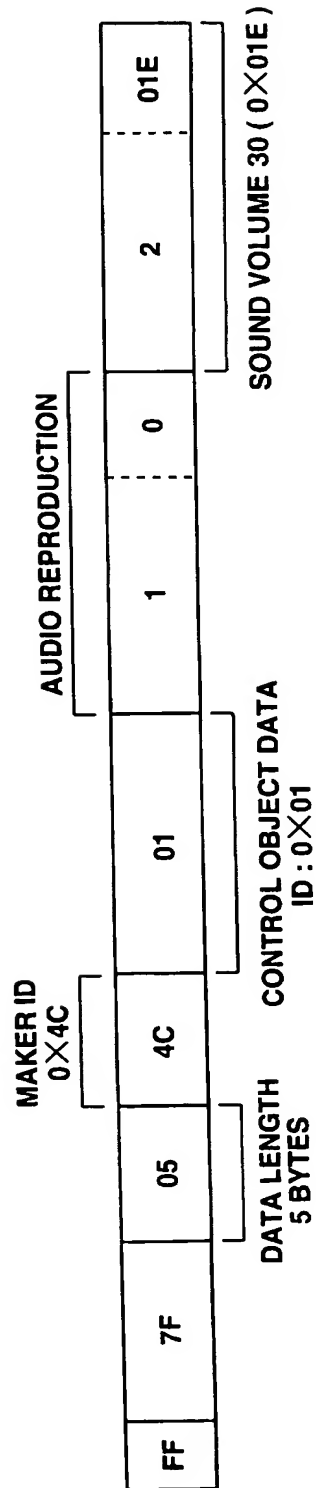


FIG.10

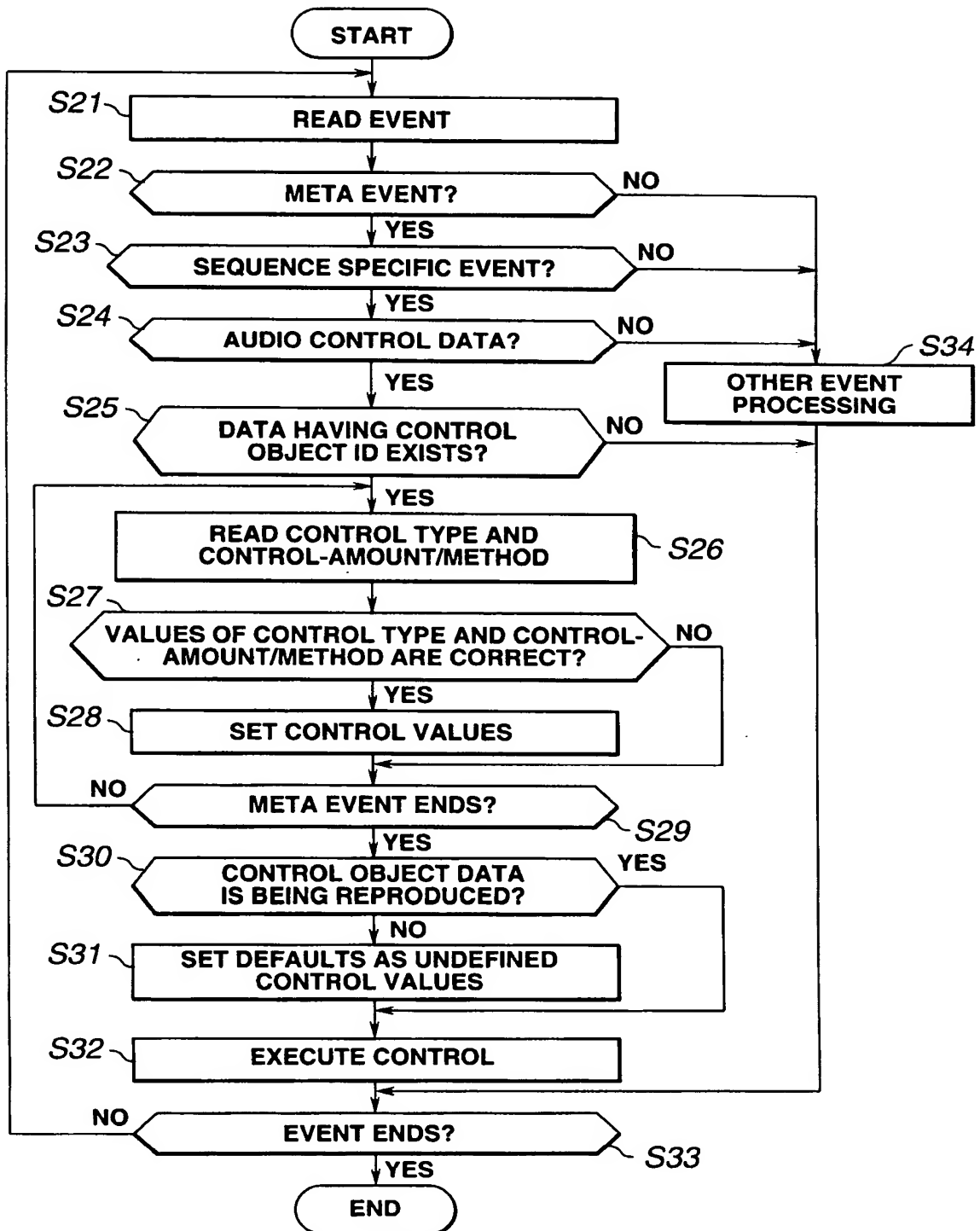


FIG.9

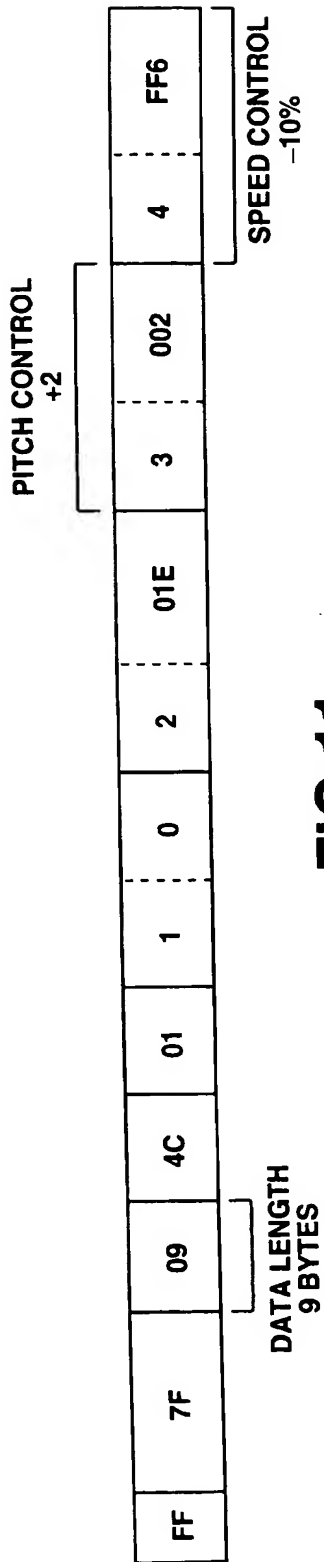


FIG.11

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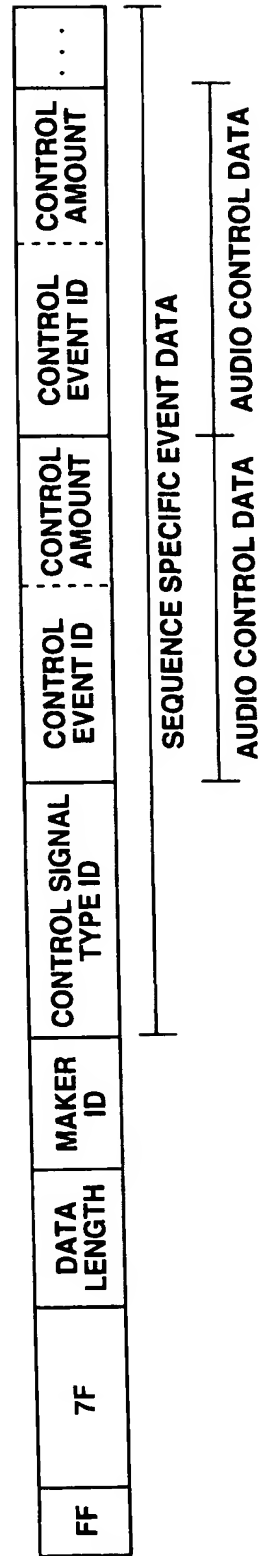


FIG.12

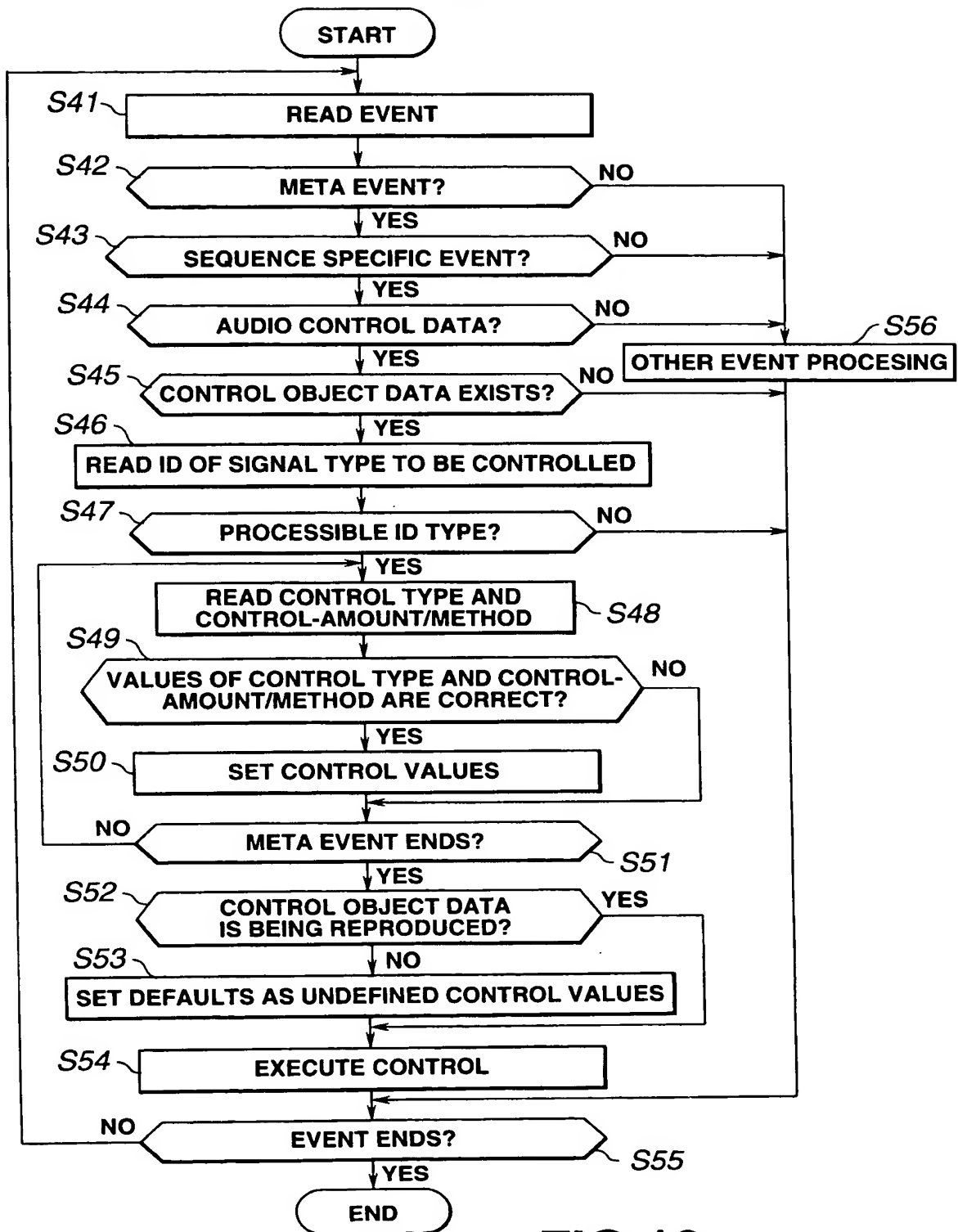


FIG.13

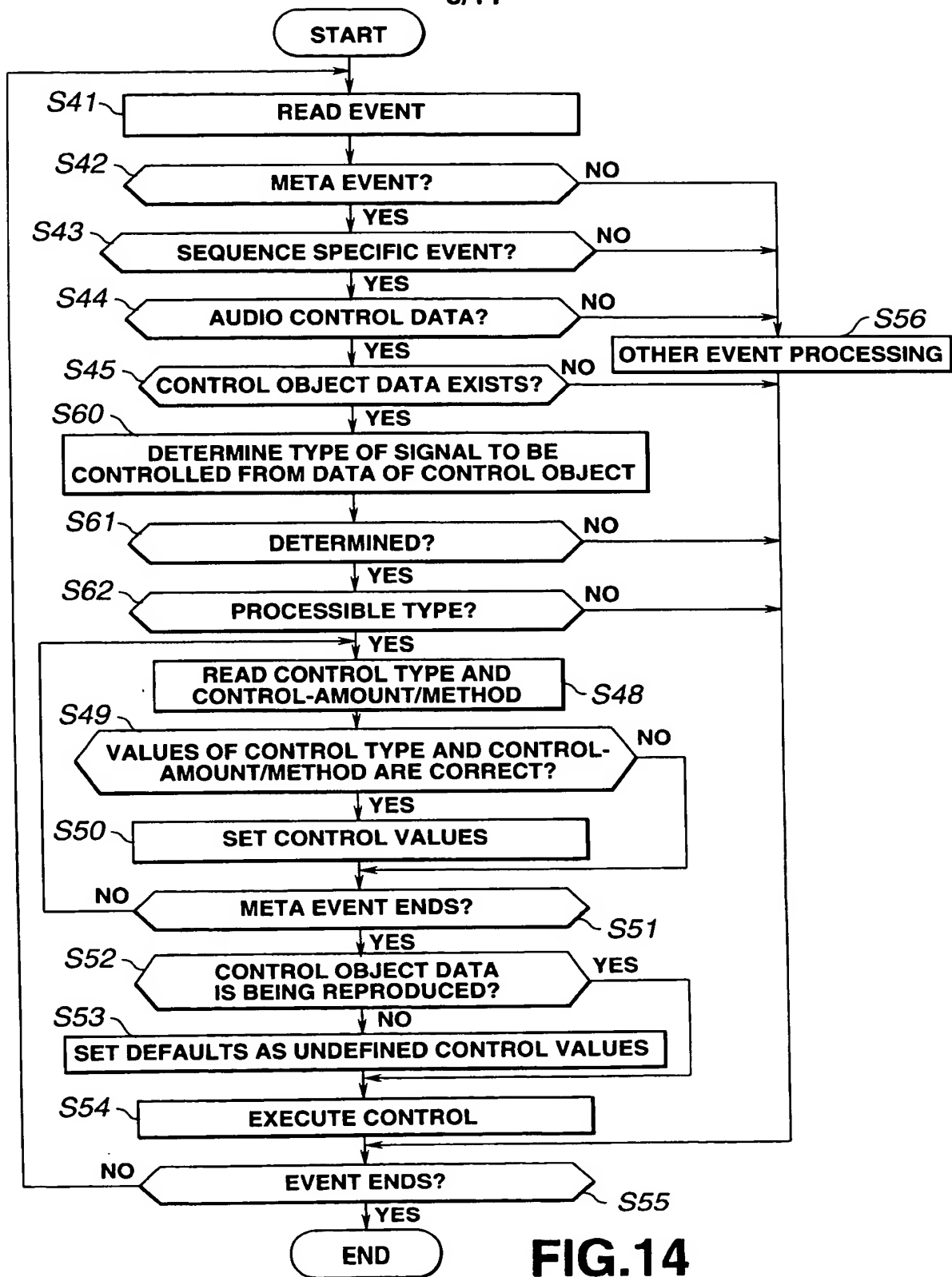


FIG.14

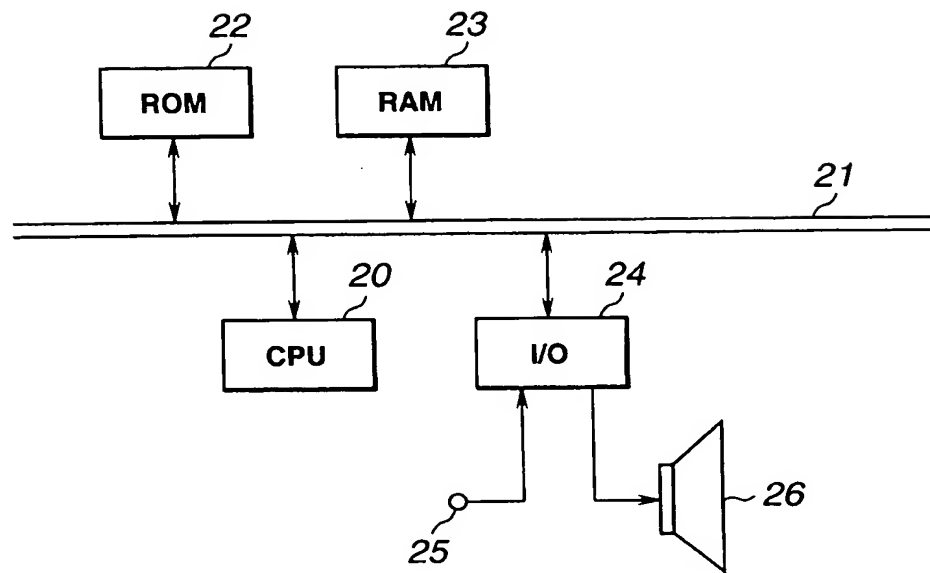
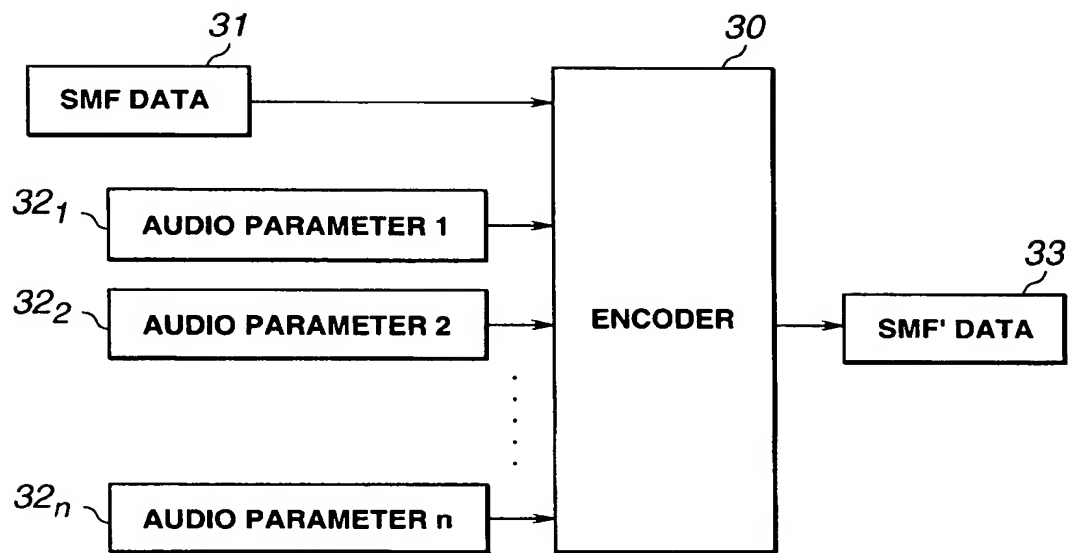


FIG.15

**FIG.16**